

IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph on page 3, between lines 4-7 of the specification with the following:

This is achieved with a cross-talk cancellation method ~~as claimed in claim 1~~, a program ~~as claimed in claim 2~~, a signal processor comprising cross-talk cancellation means ~~as claimed in claims 3 to 5~~, and an apparatus for reading a signal stored along a track on a storage medium ~~as claimed in claims 6 to 8~~ will be described.

Replace the paragraph on page 4, between lines 24-32 of the specification with the following:

Figs 1 and 2 show block diagrams of a first and a second example of an apparatus for reading such a ~~disc~~ disc 1. The apparatus shown in Fig.1 carries reference number 6-1. The apparatus of Fig.2 carries reference number 6-2. According to Figs 1 and 2, the apparatuses 6-1 and 6-2 comprise an optical unit 8

having three reading elements: a main reading element 12 for reading a main signal associated with a main track, and two satellite reading elements 11 and 13 for reading two satellite signals associated with the two tracks that are adjacent to the main track. In the subsequent description, one of these satellite signal is called upper satellite ~~signal~~ signals, and the other satellite signal is called lower satellite signal. The three reading elements transmit three light spots 21, 22 and 23.

Replace the paragraph on page 7, between lines 7-15 of the specification with the following:

For example, in some systems (mostly in writable/rewritable systems), the wobble clock can be conveniently used for estimating the ratio a . Fig.1 gives an example of an implementation that will be advantageously used in such systems: in Fig.1, the external time recovery circuit carries reference number 50-1 and is connected in the path between the main reading element 12 and the cross-talk cancellation means 42. In other systems (mostly in ROM systems), average run length measurements can be used for the same purpose. Fig.2 gives an example of an implementation that will be

advantageously used in such systems: in Fig.2, the external time recovery circuit carries reference number 50-2 and is connected in the path between the cross-talk cancellation means 42 and the decoding means 44.